

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul Elliott
Application No. : 10/658,595
Confirmation No. : 1920
Filed : September 9, 2003
For : BRIDGE CIRCUIT FOR USE IN RETIMING IN A
SEMICONDUCTOR INTEGRATED CIRCUIT

Examiner : Nimesh G. Patel
Art Unit : 2111
Docket No. : 851963.413
Date : April 22, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FEE DEFICIENCY AUTHORIZATION FORM

Commissioner for Patents:

Applicant hereby authorizes the Director to charge any deficiencies in fees due by way of the enclosed papers only under 37 CFR 1.16 and 1.17 to Deposit Account No. 19-1090.

Respectfully submitted,
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